

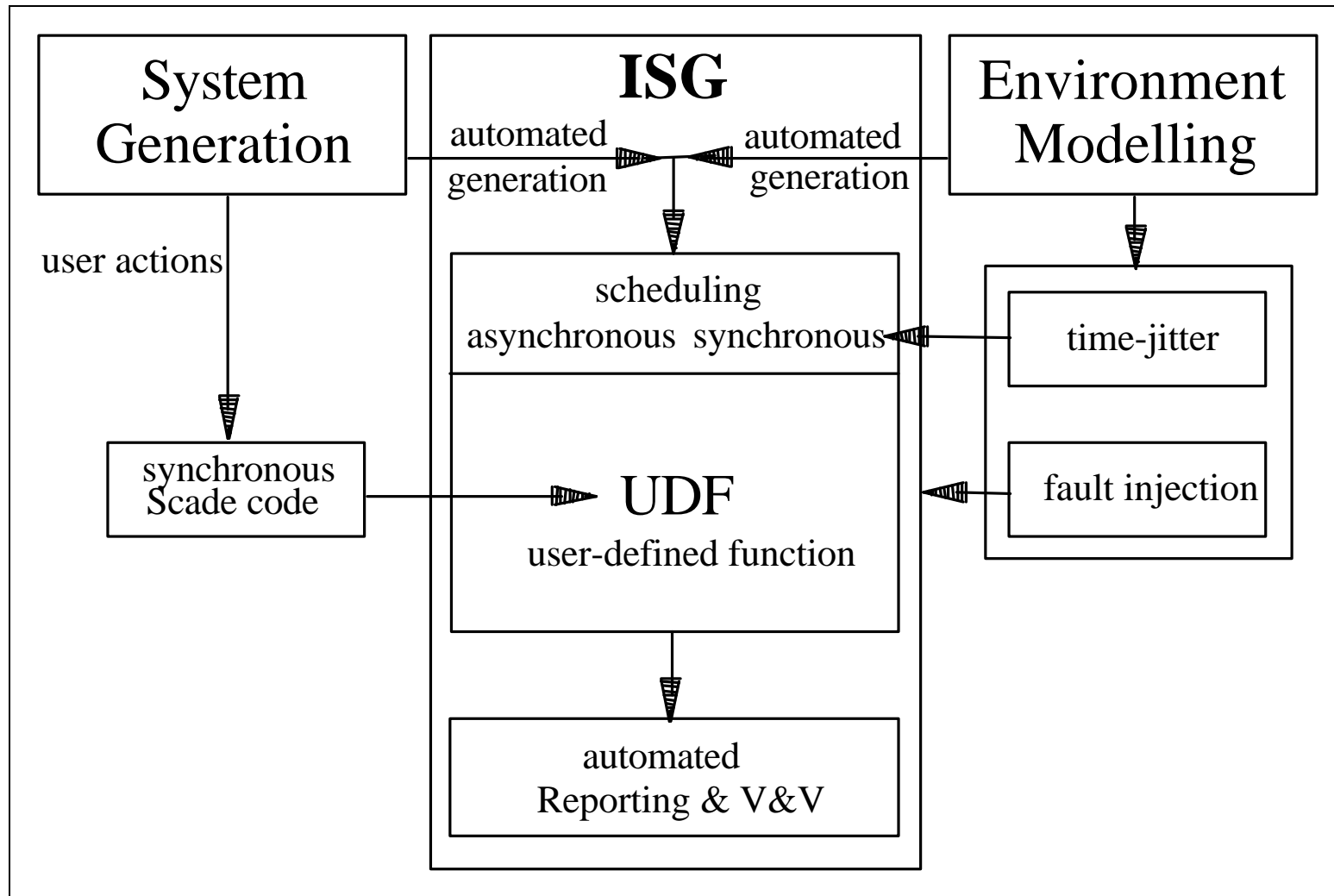
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# ISG and Synchronous Systems

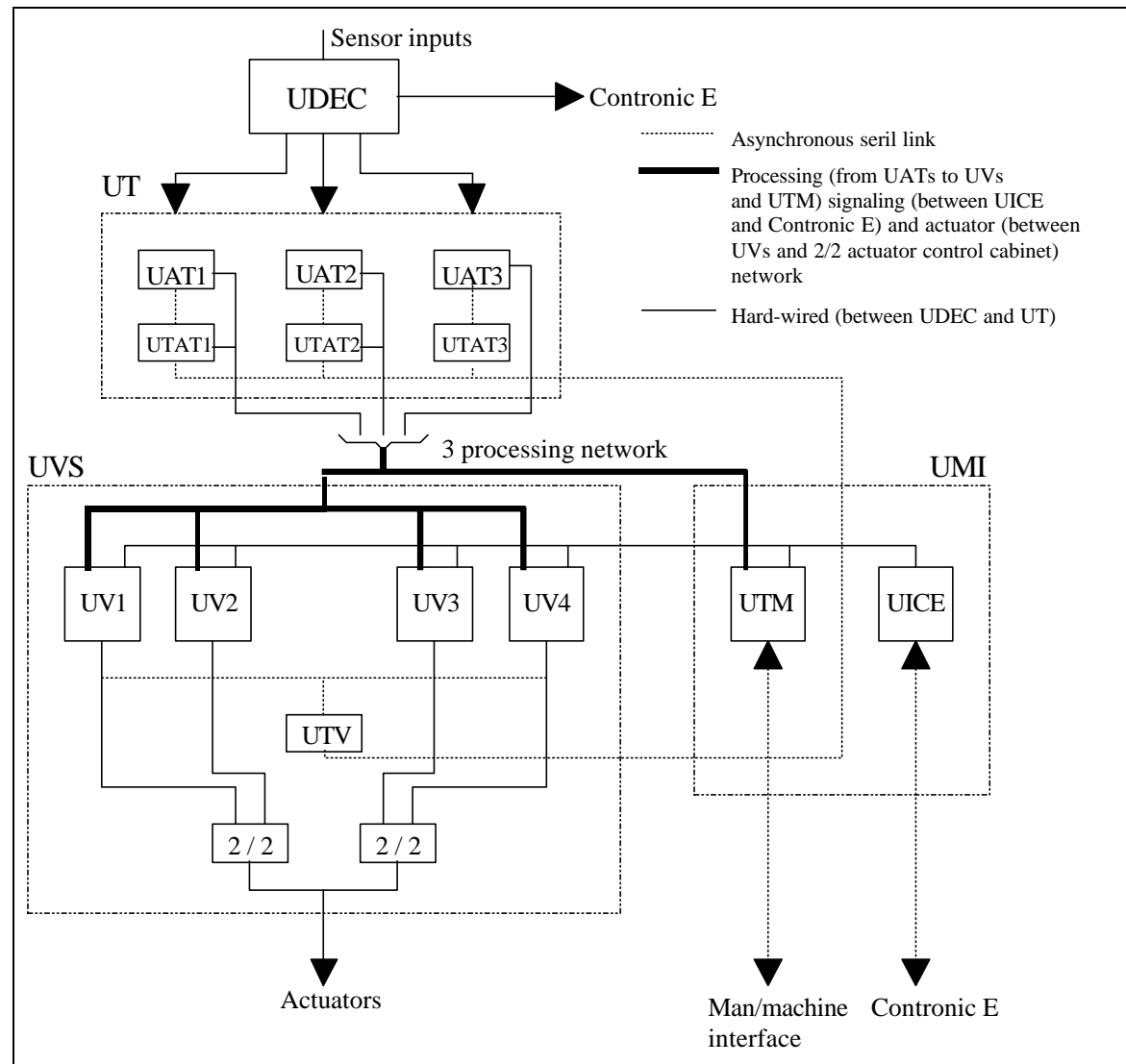
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## Principal Support by ISG

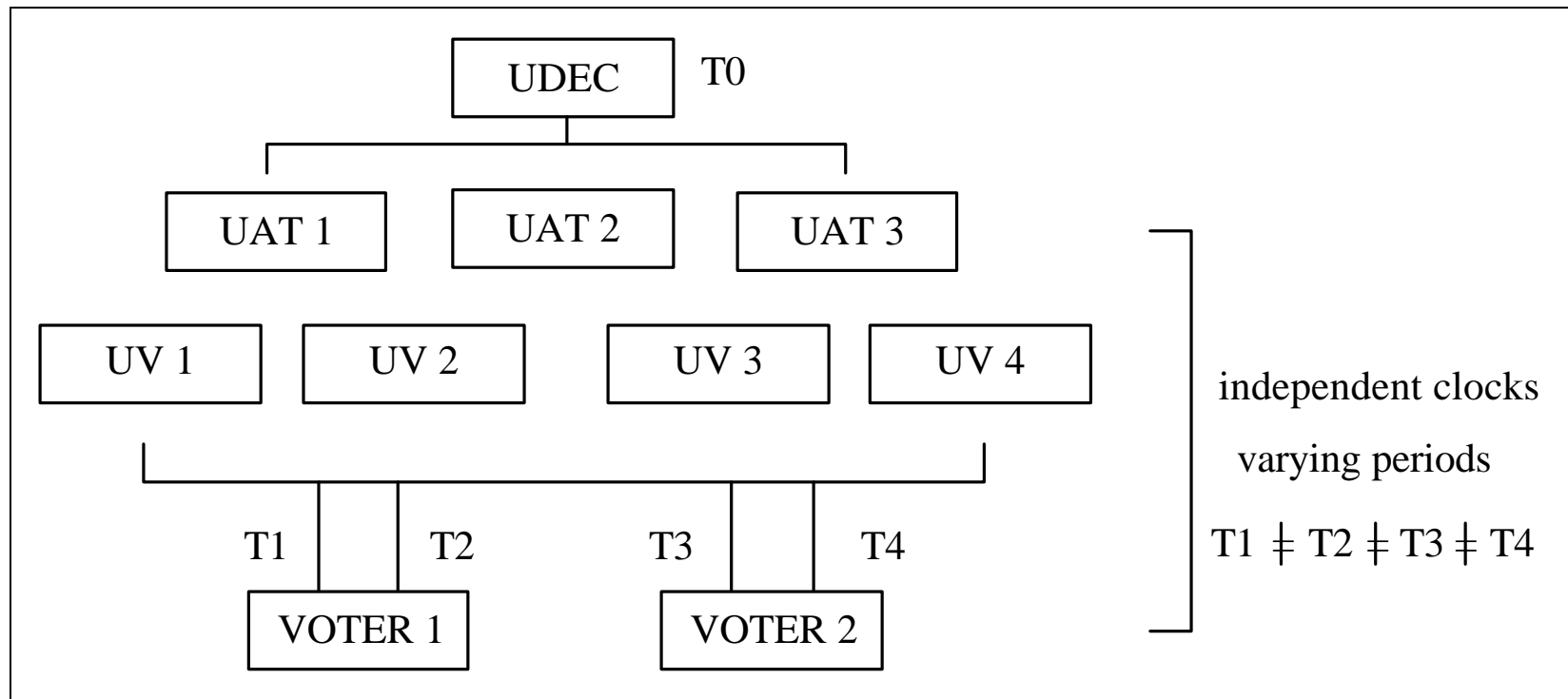


# Synchronous Application



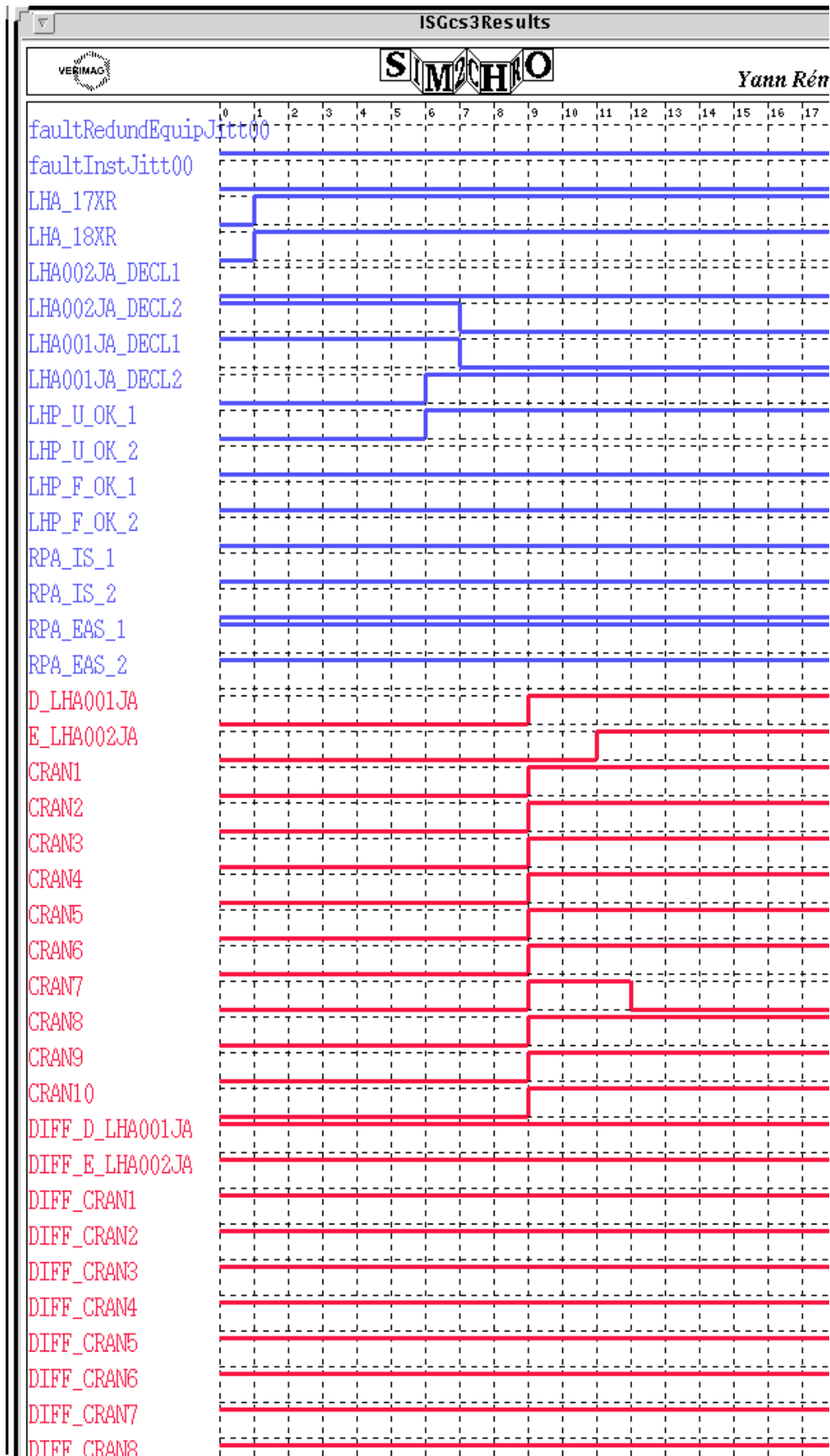
# Distribution and Propagation Delays

Stimulation (23 + 3) and Evaluation (UV 4x12, Voter 2x12)



Comparison of UV and Voter Outputs (12) per UDEC Step

# Scenario (Sim2Chro Representation)



## Generation of Time Jitter and Fault Injection

<p><b>Chain:</b> UDEC to UV, results reported to UDEC</p> <p><b>Instances:</b> UDEC: 1 UAT: 3 UV: 4 Voter: 2</p> <p><b>Inputs:</b> 26 ( UAT: 23, UV: 3) 5 pairs of redundant sensors</p> <p><b>Outputs:</b> 12 (UV)</p>	<p><b>Nodes:</b> 16 executed on one CPU</p> <p><b>Processes:</b> 16 cyclic activities: 42 one per process + 26 for stimulation</p> <p><b>Voter:</b> 2 derived by <b>AND</b> from UV no separate stage</p>
<p><b>1 minor cycle</b> = 70 steps (rate 1/s)</p> <p><b>1 major cycle</b> = iteration over 4 fault injection modes (2x2) à 10 minor cycles, in total: 5000 s</p>	
<p><b>Fault Injection:</b></p> <ul style="list-style-type: none"> <li><b>I</b> Random Input Last Instance of UAT (#3, UAT3) at UDEC</li> <li><b>S</b> Random Input Redundant Sensor (second sensor, LH*_2, RP*_2) at UDEC</li> </ul>	
<p><b>Time Jitter:</b> <i>sensor inputs (UDEC)</i> average drift 0, 0 .. 20% of period</p> <p><i>data exchange between processes</i> average drift 0, 0 .. 20% of period</p> <p style="text-align: center;">and / or</p> <p style="text-align: right;">random drift due to random period at creation time (0 .. 20%)</p>	

<b>UV</b>		<b>Sensor stimulation at beginning of cycles</b>			
		<b>S / I = No / No</b>	<b>S / I = No / Yes</b>	<b>S / I = Yes / No</b>	<b>S / I = Yes / Yes</b>
<b>Steps</b>	<b>Time Jitter</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>
3991	none	1691, 20292, 80 <b>0.4</b>	300, 3600, 39 <b>1.1</b>	1574, 18888, 3319 <b>17.6</b>	426, 5112, 1016 <b>19.9</b>
4994	none (update)	1419, 17028, 0 <b>0</b>	1420, 17040, 28 <b>0.2</b>	1420, 17040, 3147 <b>18.5</b>	735, 8820, 1879 <b>21.3</b>
5013	± 0.5 %	1701, 20412, 4284 <b>21.0</b>	772, 9264, 1416 <b>15.3</b>	1522, 18264, 6701 <b>36.7</b>	1018, 12216, 4286 <b>35.1</b>
5071	± 2 %	1715, 20580, 3363 <b>16.4</b>	766, 9192, 1582 <b>17.2</b>	1571, 18852, 6471 <b>34.3</b>	1019, 12228, 4060 <b>33.2</b>
4157	± 5 %	1794, 21528, 3650 <b>17.0</b>	303, 3636, 648 <b>17.8</b>	1559, 18708, 5999 <b>32.1</b>	501, 6012, 1736 <b>28.9</b>
4329	± 10 %	1117, 13404, 2255 <b>16.8</b>	1048, 12576, 2246 <b>17.9</b>	1104, 13428, 4021 <b>30.4</b>	1060, 12720, 4523 <b>35.6</b>
5124	± 20 %	2053, 24636, 5048 <b>20.5</b>	551, 6612, 1309 <b>19.8</b>	1754, 21048, 7761 <b>36.9</b>	766, 9192, 2880 <b>31.3</b>

<b>UV</b>		<b>Sensor stimulation in the middle of the cycles</b>			
		<b>S / I = No / No</b>	<b>S / I = No / Yes</b>	<b>S / I = Yes / No</b>	<b>S / I = Yes / Yes</b>
<b>Total Steps</b>	<b>Time Jitter</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>
	none				
5074	± 2 %	1733, 20796, 3082 <b>14.8</b>	771, 9252, 1345 <b>14.5</b>	1553, 18636, 6475 <b>34.7</b>	1017, 12204, 4331 <b>35.5</b>
	± 5 %				
	± 10 %				
	± 20 %				

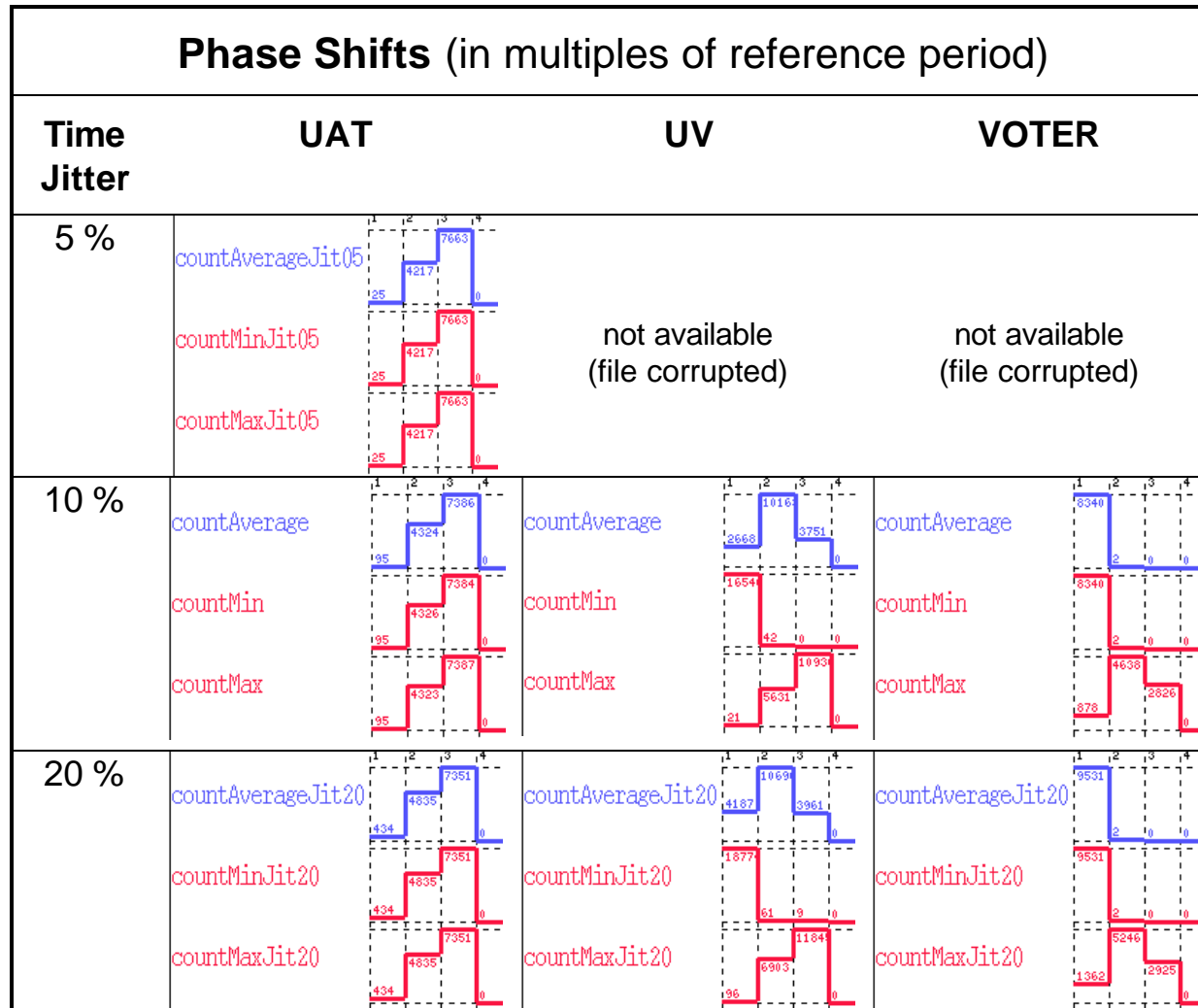


<b>UV</b>		<b>±10 % period variation</b>			
		<b>S / I = No / No</b>	<b>S / I = No / Yes</b>	<b>S / I = Yes / No</b>	<b>S / I = Yes / Yes</b>
<b>Total Steps</b>	<b>Time Jitter</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps, Count, Diffs %</b>	<b>Steps Count, Diffs %</b>
4329	sensor and data exchange	1117, 13404, 2255 <b>16.8</b>	1048, 12576, 2246 <b>17.9</b>	1104, 13428, 4021 <b>30.4</b>	1060, 12720, 4523 <b>35.6</b>
4991	<b>only</b> sensor stimulation with time jitter	1729, 20748, 94 <b>0.5</b>	790, 9480, 22 <b>0.2</b>	1495, 17940, 3039 <b>16.9</b>	977, 11724, 2026 <b>17.3</b>
5510	sensor 0% data exchange with time jitter and drift	1932, 23184, 4078 <b>17.6</b>	804, 9648, 1965 <b>20.4</b>	1678, 20136, 7205 <b>35.8</b>	1096, 13152, 4230 <b>32.2</b>
5514	sensor 0%, data exchange w/o time jitter, but drift	1929, 23148, 4218 <b>18.2</b>	797, 9564, 1825 <b>19.1</b>	1678, 20136, 7079 <b>35.2</b>	1110, 13320, 4914 <b>36.9</b>
5517	same as above, but different FI scenario	3607, 43284, 7871 <b>18.2</b>	1910, 22920, 4484 <b>19.6</b>	-	-
5112	sensor 0% data exchange w/o drift, but jitter	1450, 17400, 3353 <b>19.3</b>	1453, 17436, 3547 <b>20.3</b>	1454, 17448, 6198 <b>35.5</b>	755, 9060, 3327 <b>36.7</b>

<b>VOTER</b>		<b>Sensor stimulation at beginning of cycles</b>			
		<b>S / I = No / No</b>	<b>S / I = No / Yes</b>	<b>S / I = Yes / No</b>	<b>S / I = Yes / Yes</b>
<b>Total Steps</b>	<b>Time Jitter</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>
4994	none (update)	1419, 17028, 0 <b>0</b>	1420, 17040, 11 <b>0.1</b>	1420, 17040, 2087 <b>12.2</b>	735, 8820, 1376 <b>15.6</b>
	± 0.5 %				
	± 2 %				
	± 5 %				
	± 10 %				
	± 20 %				

<b>VOTER</b>					
		<b>S / I = No / No</b>	<b>S / I = No / Yes</b>	<b>S / I = Yes / No</b>	<b>S / I = Yes / Yes</b>
<b>Total Steps</b>	<b>Time Jitter</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>	<b>Steps Count Diffs %</b>
	sensor and data exchange				
	sensor stimulation with time jitter				
	sensor 0% data exchange with time jitter and drift				
	sensor 0%, data exchange w/o time jitter, but drift				
5517	same as above, but different FI scenario	3607, 43284, 3455 <b>7.9</b>	1910, 22920, 1980 <b>8.6</b>	-	-
5512	sensor 0% data exchange w/o drift, but jitter	1450, 17400, 1464 <b>8.4</b>	1453, 17436, 1534 <b>8.8</b>	1454, 17448, 3370 <b>19.3</b>	755, 9060, 1967 <b>21.7</b>

# Histograms on Phase Shifts (Propagation Delay)



## Results of Fault Injection and Time Jitter

- insensitive** against time jitter at sensor data acquisition
- insensitive** against one wrong data input (last instance of UAT and UV from UDEC)
- very sensitive** against wrong data of second redundant sensor (if available)
- very sensitive** against time jitter (of any type) of cyclic processing
- is observed sensitivity a matter of control logic ?